run all experiments twice. The FID shown in Figure 4 uses two filters with different settings. The pulser is programmed to select the output synchronously. Generators feature 32-bit frequency and 16-bit phase control, 14-bit outputs for Digital Analog Converters and the capability to run DDS RF generators. Among the DDS RF sources, there are two digital RF sources routed to three RF output connectors. Apart from the DDS generators, there are other two digital RF sources. The new board gives us the possibility to combine the desirable features of both situations while avoiding the necessity to buffer TTL output ports and the capability to host an extension board for special/prototyping purposes.

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